

Fig. 1

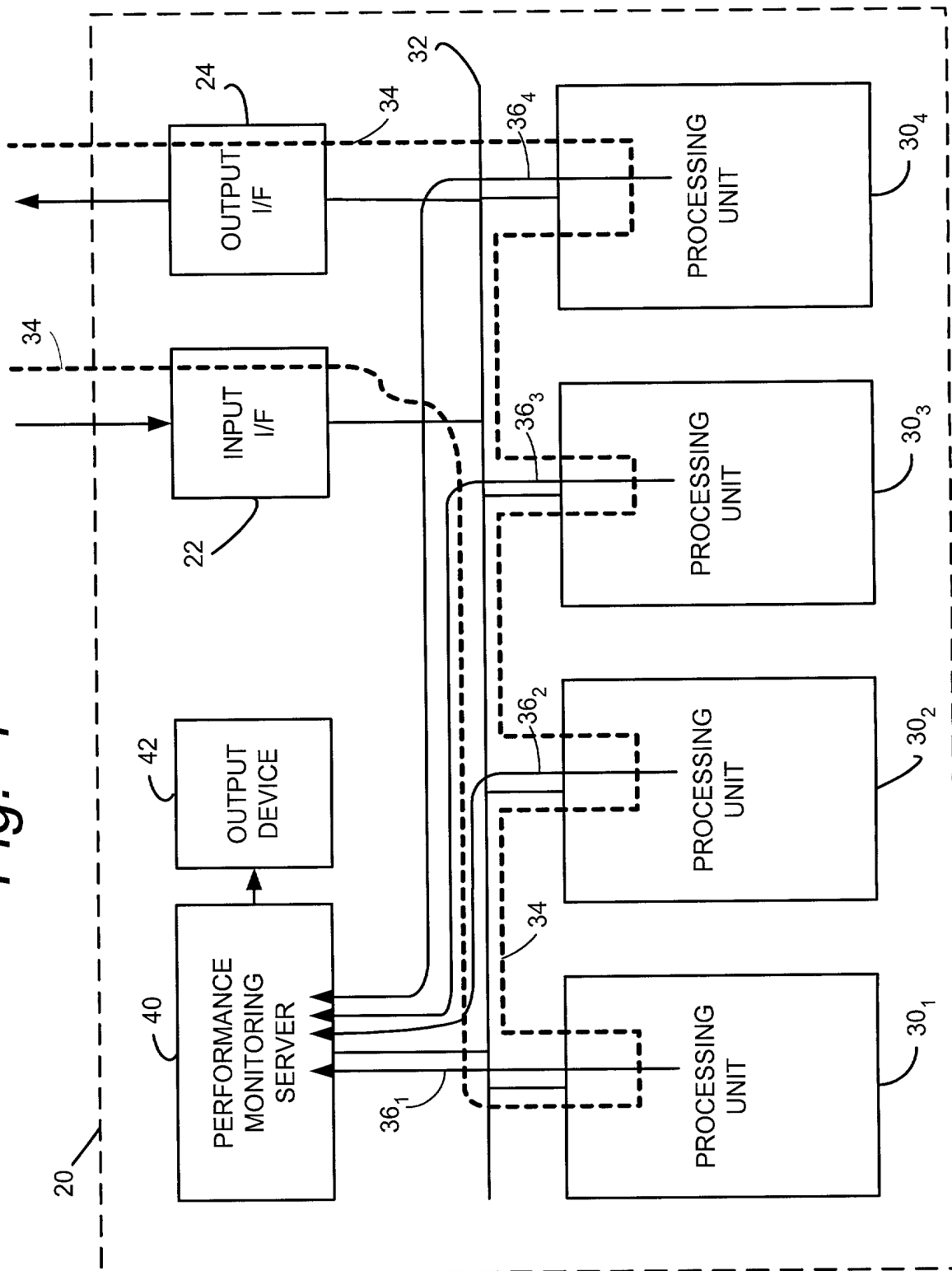


Fig. 2

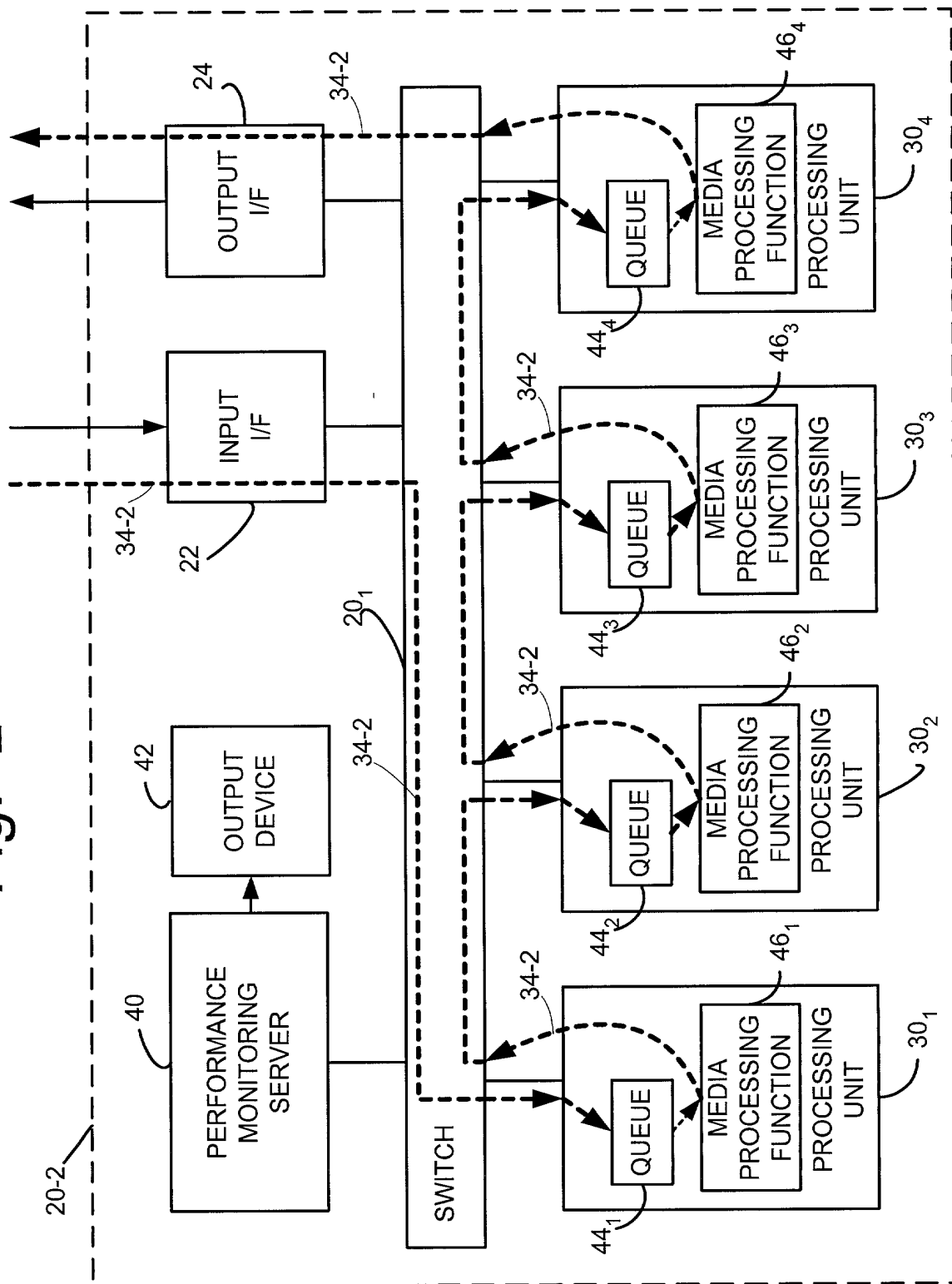


Fig. 3

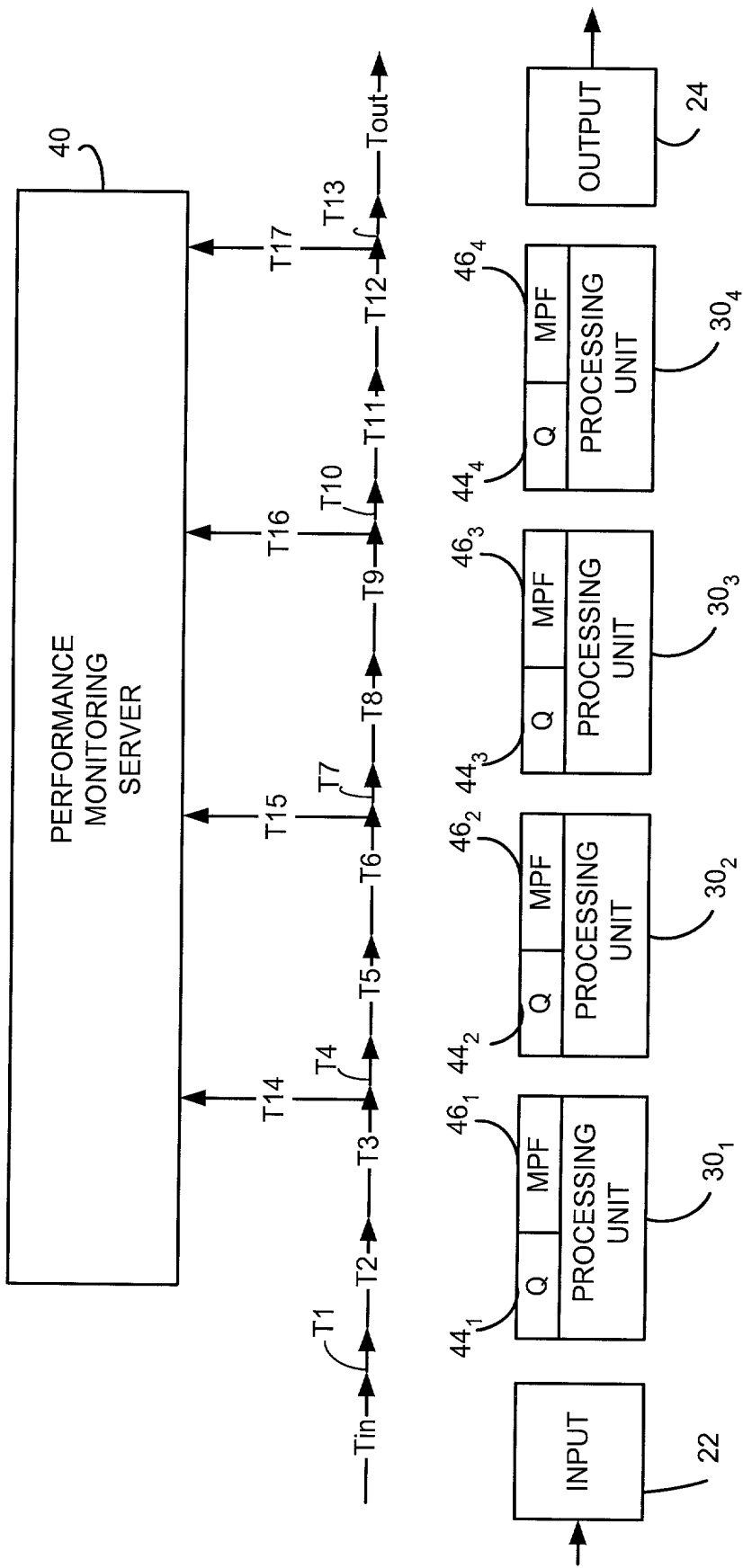
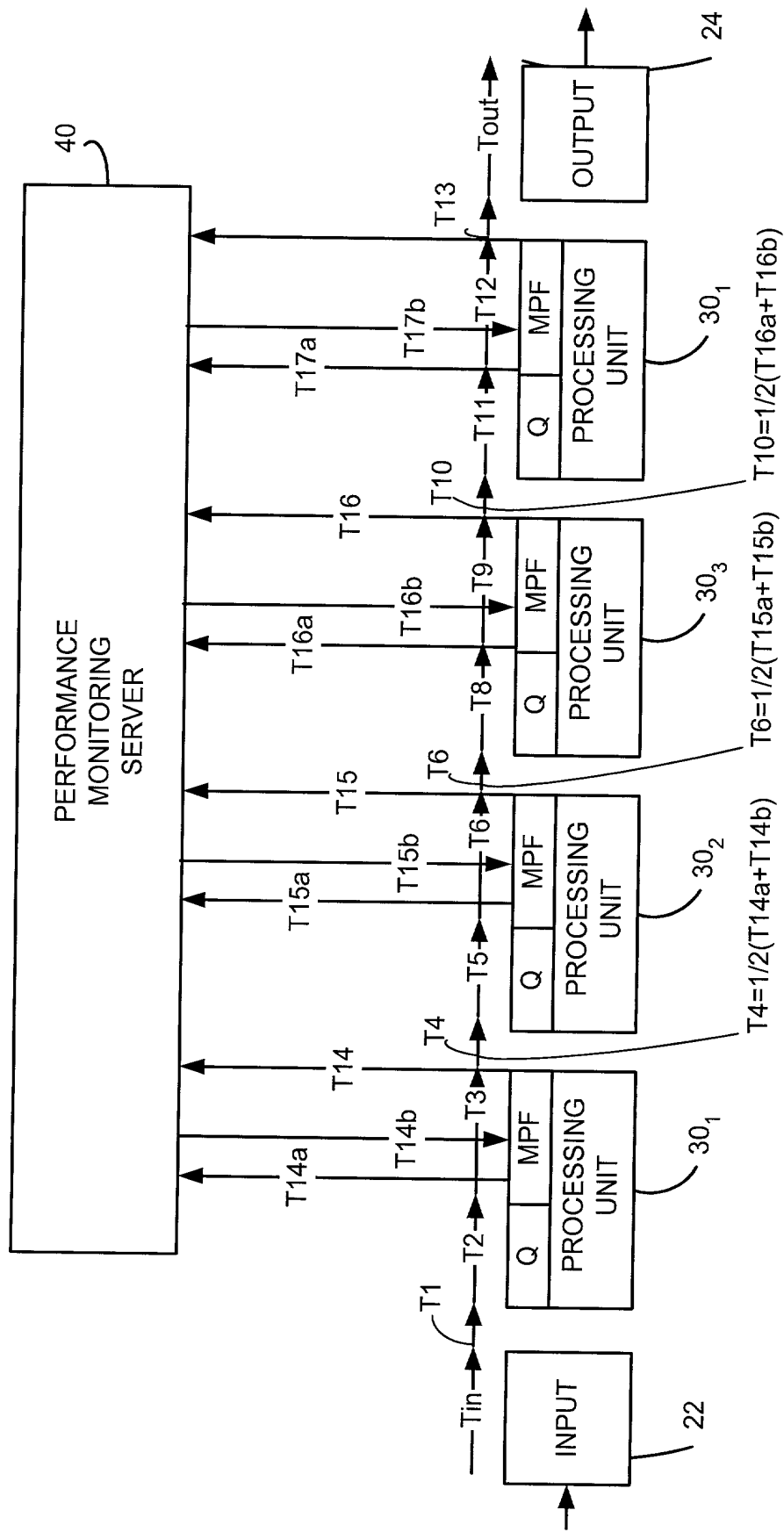


Fig. 4



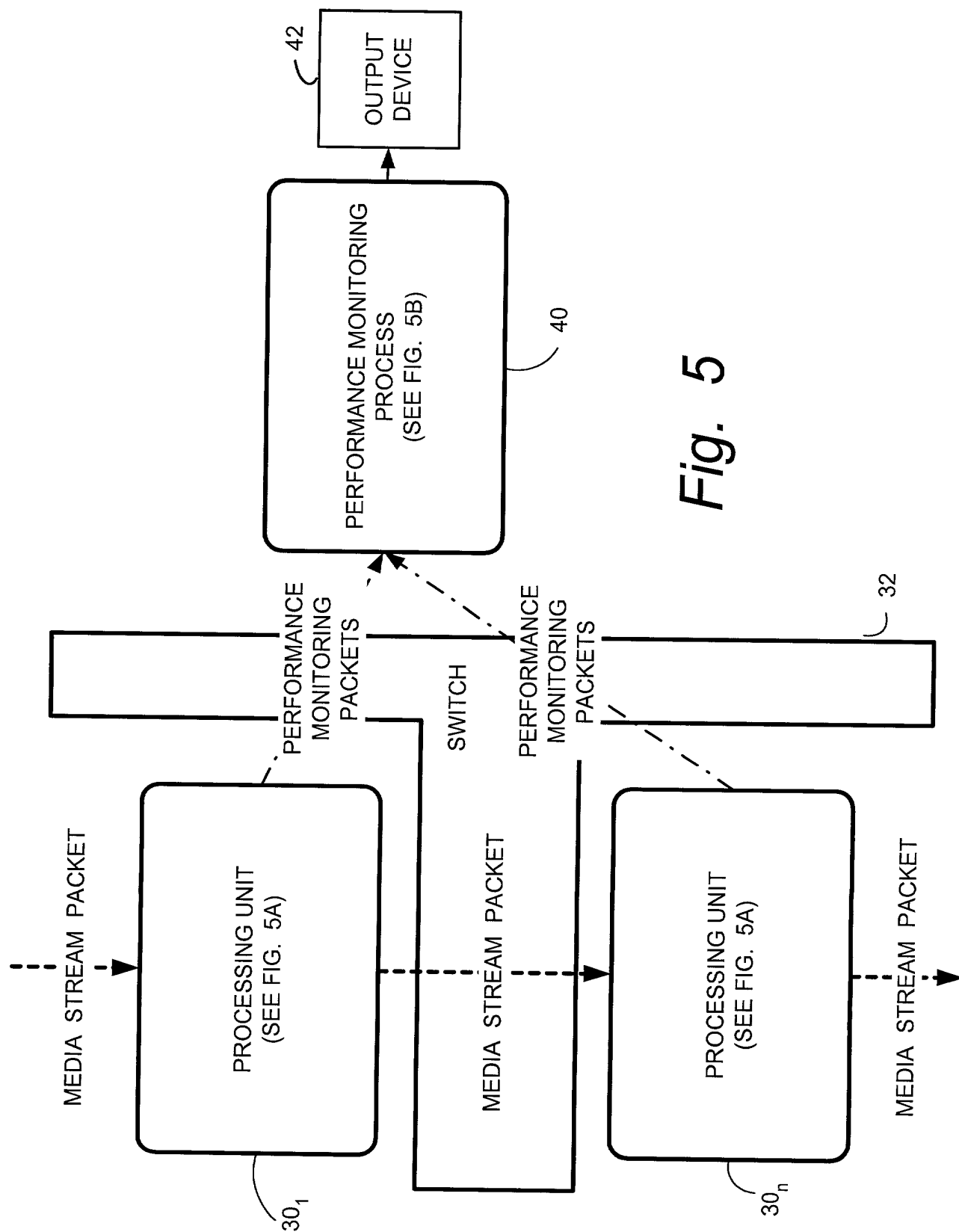


Fig. 5

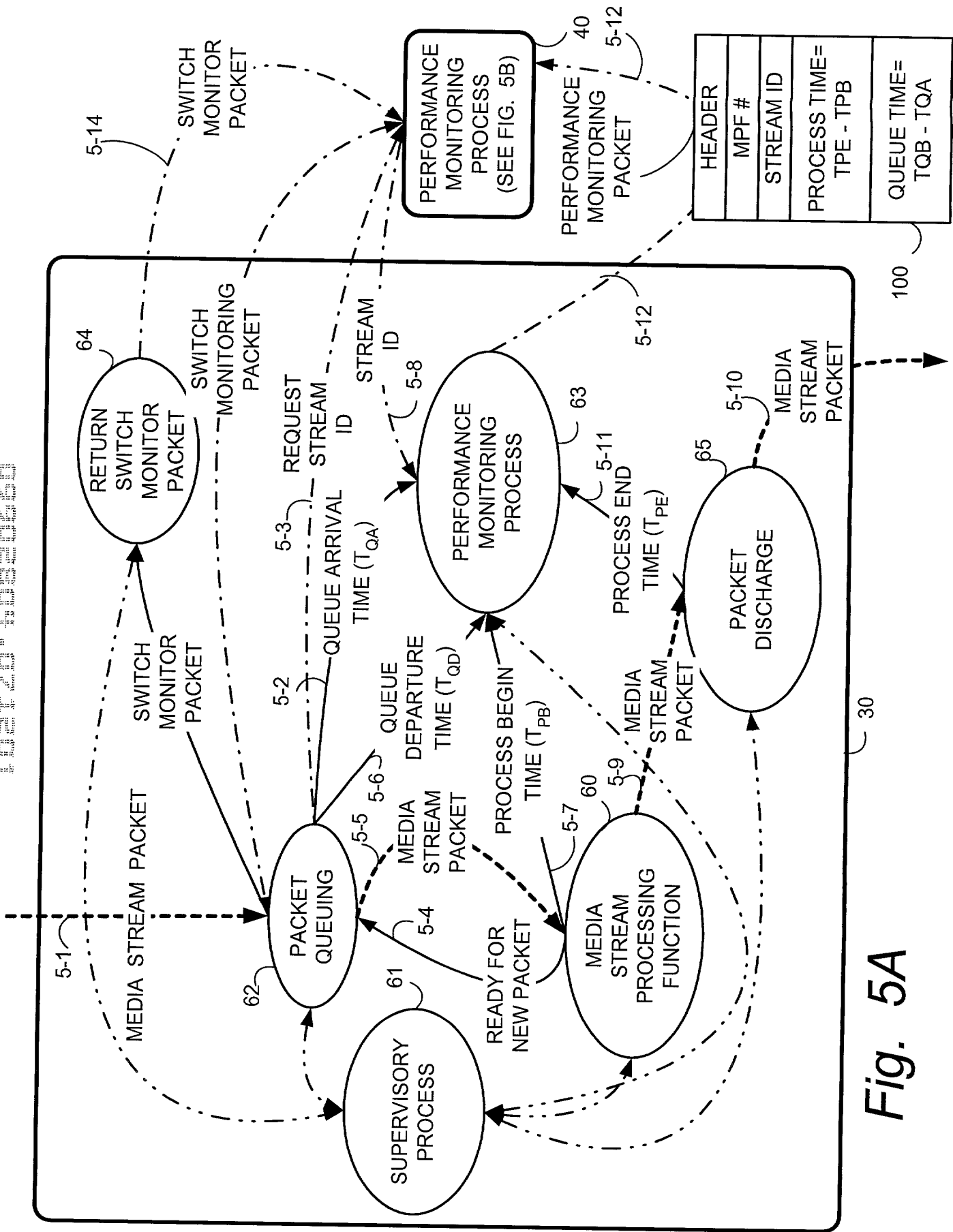


Fig. 5A



Fig. 5B

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OVERALL NODE DELAY	OND ₂
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Fig. 6

Fig. 7

